	FORM PTO-1449		ATTY. DOCKET NO. 1778.2110000	APPLICATION NO. 09/788,670
	SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		FIRST NAMED INVENTOR Hartvig W.J. Ekner	
			FILING DATE February 21, 2001	ART UNIT 2193

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EXAMINER INITIAL	DOC. REF.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
CD	US1	2003/0172254 A1	09/2003	Mandavilli <i>et al.</i>			
	US2	4,491,910	01/1985	Caudel <i>et al.</i>			
	US3	4,511,990	04/1985	Hagiwara <i>et al.</i>			
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	US7	5,185,713	02/1993	Kobunaya			
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	US10	5,669,010	09/1997	Duluk, Jr.			
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CD	FP1	EP 0 681 236 A1	11/1995	EP			N/A
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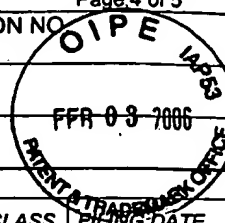
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CD	NPL1	88410 Second Level Cache, Microprocessor Forum, Motorola Inc., November 1991, 20 pages (Bates Numbers L00622-L00641).
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	US15	5,740,340	04/1998	Purcell <i>et al.</i>			
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	US22	5,822,606	10/1998	Morton			
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CD	FP3	WO 97/07450 A1	02/1997	PCT			N/A
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EXAMINER INITIAL	DOC. REF.	CITATION					
CD	NPL6	El-Sharkawy, Mohamed, Ph.D., <i>Digital Signal Processing Applications with Motorola's DSP56002 Processor</i> , Prentice Hall PTR, 1996, pages 43-124 (Bates Numbers L06519-L06601).					
CD	NPL7	Lijun Gao, Sarvesh Shrivastava, Hanbo Lee, Gerald E. Sobelman, "A Compact Fast Variable Key Size Elliptic Curve Cryptosystem Coprocessor," <i>Proceedings of the 7th Annual IEEE Symposium on Field-Programmable Custom Computing Machines</i> , April 21-23, 1999, pp. 304-305 (4 pages).					
CD	NPL8	Gwennap, L., "Digital, MIPS Add Multimedia Extensions," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 15, November 18, 1996, pages 1-5.					
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CD	NPL10	Hansen, Craig, "Architecture of a Broadband Mediaprocessor," <i>COMPCON96</i> , February 25-29, IEEE, 1996, 8 pages (Bates Numbers L00654-L00661).					
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EXAMINER INITIAL	DOC. REF.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	FP						Yes No
	FP						Yes No

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CD	NPL16	Lexra LX5280 Seminar Slides, date unknown, 49 pages.
CD	NPL17	LX4180, at http://www.lexra.com/lx4180.html , Lexra Inc., 9 pages (Copyright 1998-1999).
CD	NPL18	Peleg, A. and Weiser, U., "MMX Technology Extension to the Intel Architecture," <i>IEEE Micro</i> , IEEE, August 1996, pages 42-50 (Bates Numbers L07222-L07230).
CD	NPL19	Piccolo - The ARM Signal Processing Architecture, at http://www.calab.cs.pusan.ac.kr/TechnicalReport.... , 12 pages (visited March 6, 2001).
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EXAMINER INITIAL	DOC. REF.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	FP						Yes No
	FP						Yes No
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CD	NPL22	TMS32010 User's Guide, Texas Instruments Incorporated, 1983, page 1183.					
CD	NPL23	Turley, Jim and Hakkarainen, Harri, "TI's New 'C6x DSP Screams at 1,600 MIPS," <i>Microprocessor Report</i> , MicroDesign Resources, February 17, 1997, pages 14-17.					
CD	NPL24	VIS TM Instruction Set User's Manual, Sun Microsystems, Inc., 1997, pages i-xii and 1-136.					
CD	NPL25	Yarlagadda, Krishna, "Lexra Adds DSP Extensions," <i>Microprocessor Report</i> , MicroDesign Resources Inc., Vol. 13, Issue 11, August 23, 1999, pages 19-21 (printed from West database, 6 pages).					
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